



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Kimoto, Hisamitsu

Serial No.: 09/850,254

Group Art Unit: 2836

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Examiner: Quinto, Kevin V.

Title: Semiconductor Apparatus and
Manufacturing Method Therefor

Attorney Docket No.: N13476100S

RESPONSE TO OFFICE ACTION

5 Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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A. Introductory Comments

The following is submitted in response to the Office Action dated September 04, 2003,
and is currently due December 4, 2003.

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37 C.F.R. §1.8

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